



Company Profile

Fractal Technologies Mission & Vision



Mission

Fractal Technologies is dedicated to provide high quality solutions and support to enable their Customers to validate the quality of internal and external IP's and Libraries. Thanks to our validation solutions, Fractal Technologies maximize value for its Customers either at the Sign Off stage, for incoming inspection or on a daily basis within the Design Flow process

Vision

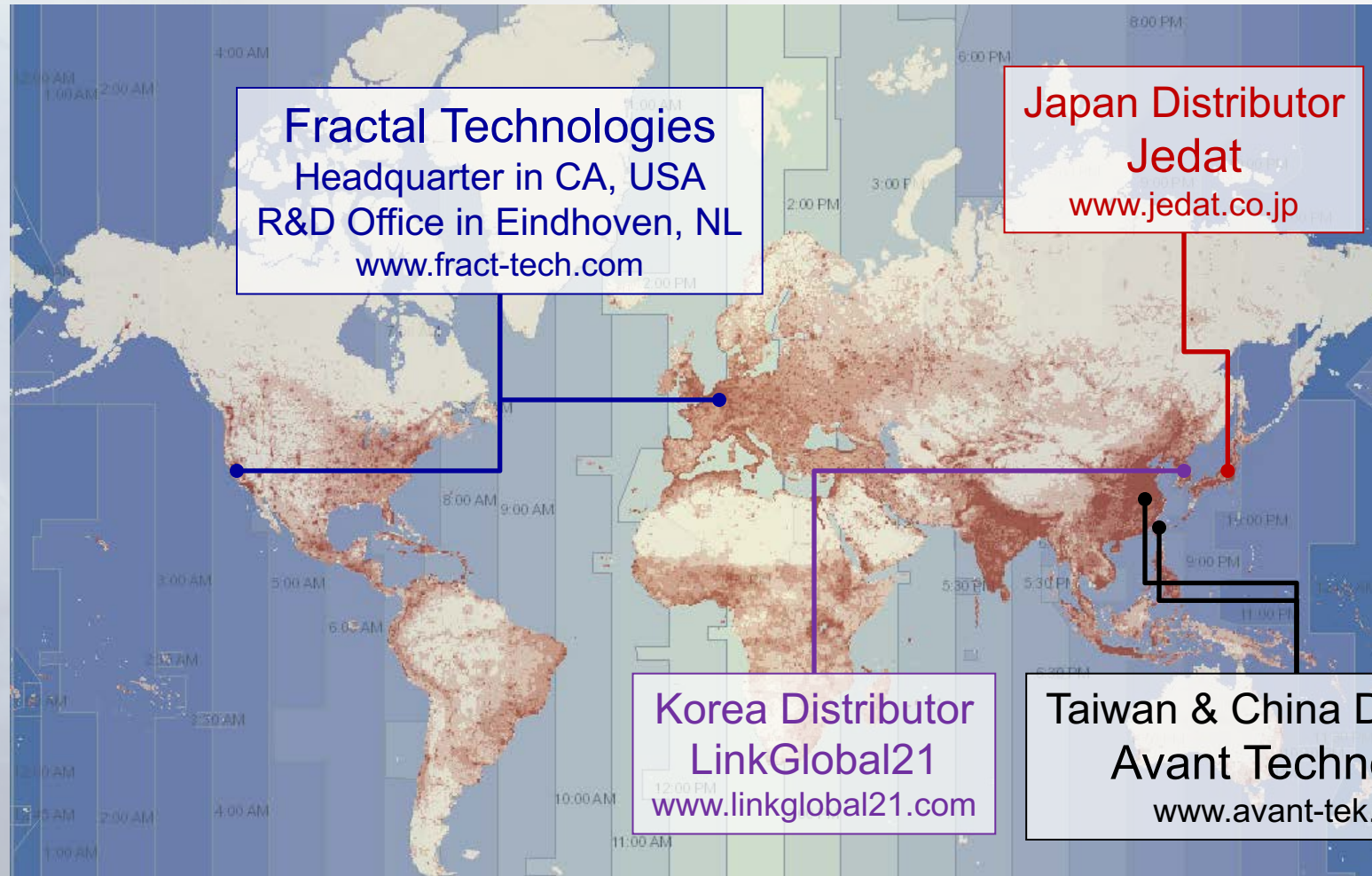
Fractal Technologies goal is to become the de facto IP & Library Validation Solutions Provider of reference for the Semiconductors Industry, while staying independent to keep its intrinsic value by delivering comprehensive, easy to use and flexible products

Fractal Technologies Profile



- Privately Held and Independently Owned
 - No Venture Capital or Angels Investment
 - Only Strategic Investments would be considered
 - The goal is to keep the company independent in its choices and purely driven by customer dedication
- Focused, Lean, Flexible and Highly Efficient Cost Structure
- Fractal Technologies is based on High Quality Standard Products and on a Seasoned Team

Fractal Technologies Eco-System



Fractal Technologies
Headquarter in CA, USA
R&D Office in Eindhoven, NL
www.fract-tech.com

Japan Distributor
Jedat
www.jedat.co.jp

Korea Distributor
LinkGlobal21
www.linkglobal21.com

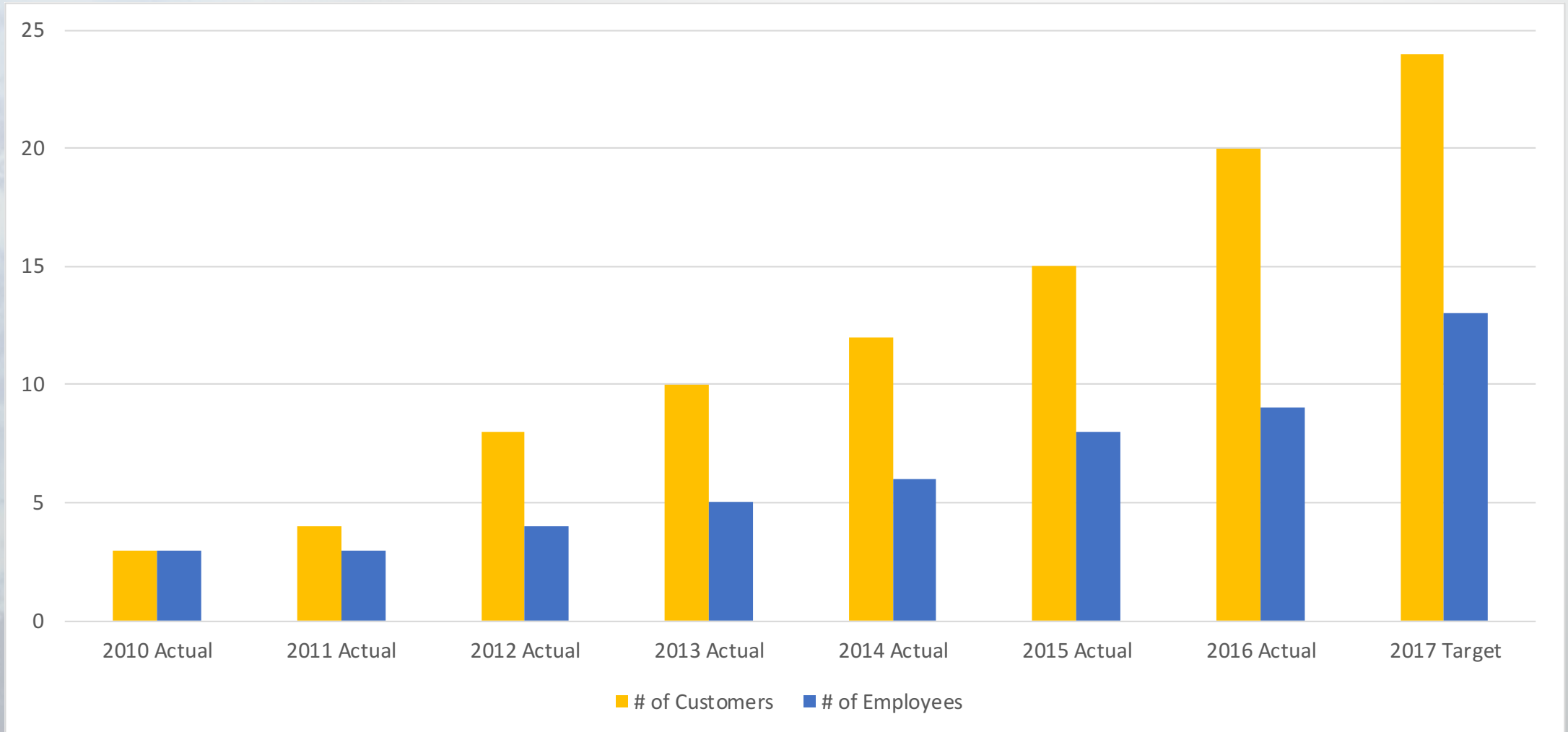
Taiwan & China Distributor
Avant Technology
www.avant-tek.com

Fractal Technologies Market Penetration Strategy



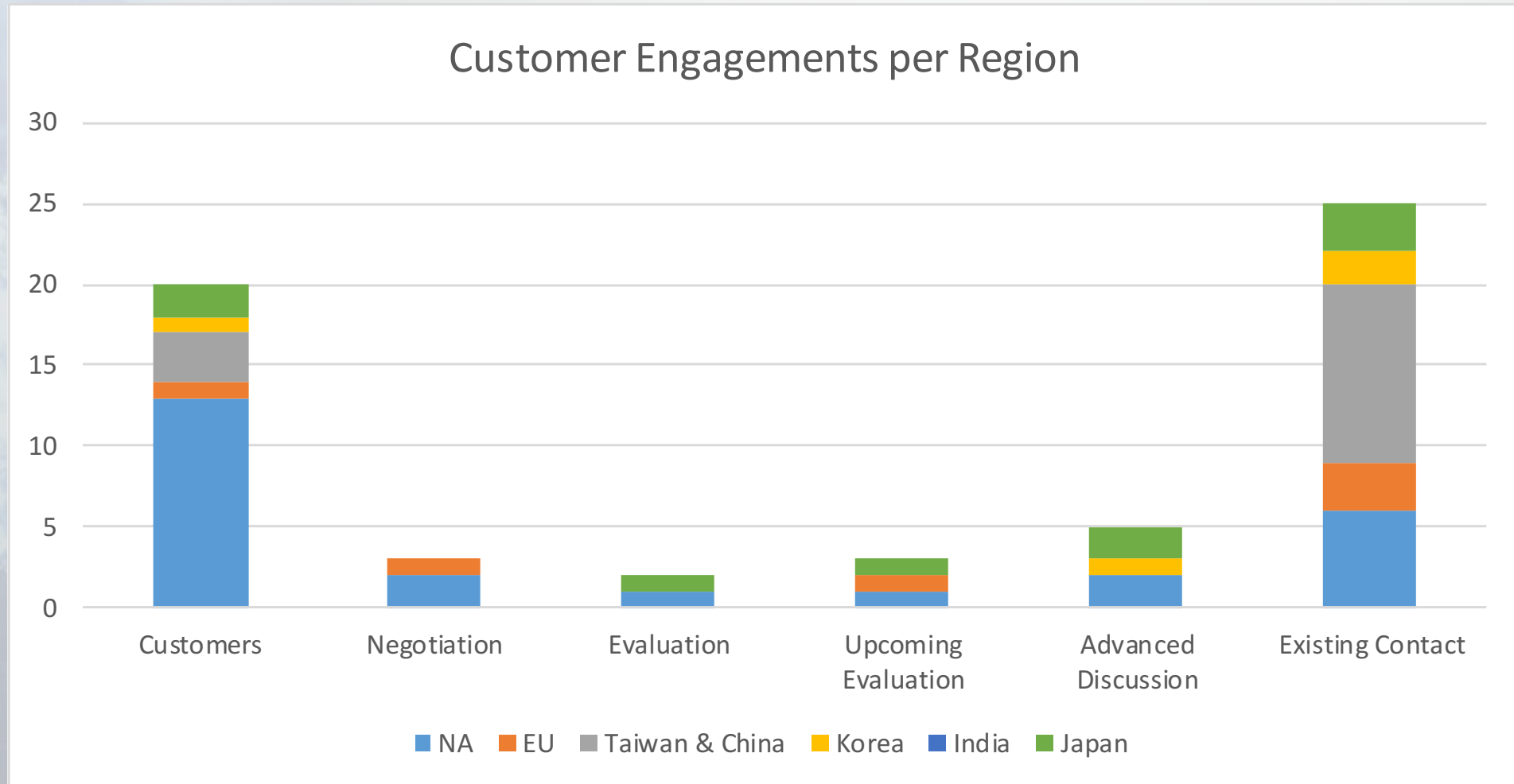
- Fractal Technologies is focusing on top tier semiconductor companies who can leverage our SIP Validation solution across the board
 - At first, Fractal is working with their customers on developing and productizing their upgraded validation platform
 - Then, Fractal is helping their customers proliferating the validation platform at the enterprise level
 - Lastly, Fractal and their customers are working on enabling customers' SIP providers to strengthen the full SIP eco-system
- Fractal Technologies primary market penetration strategy is currently focusing on
 - Major Fabless companies
 - Major Foundries
 - Major IDM
- Such strategy is starting to enable Fractal to be adopted by our customers' 3rd party IP providers
- Our ultimate goal is to become the cornerstone of the major SIP ecos-systems

Company Organic Growth



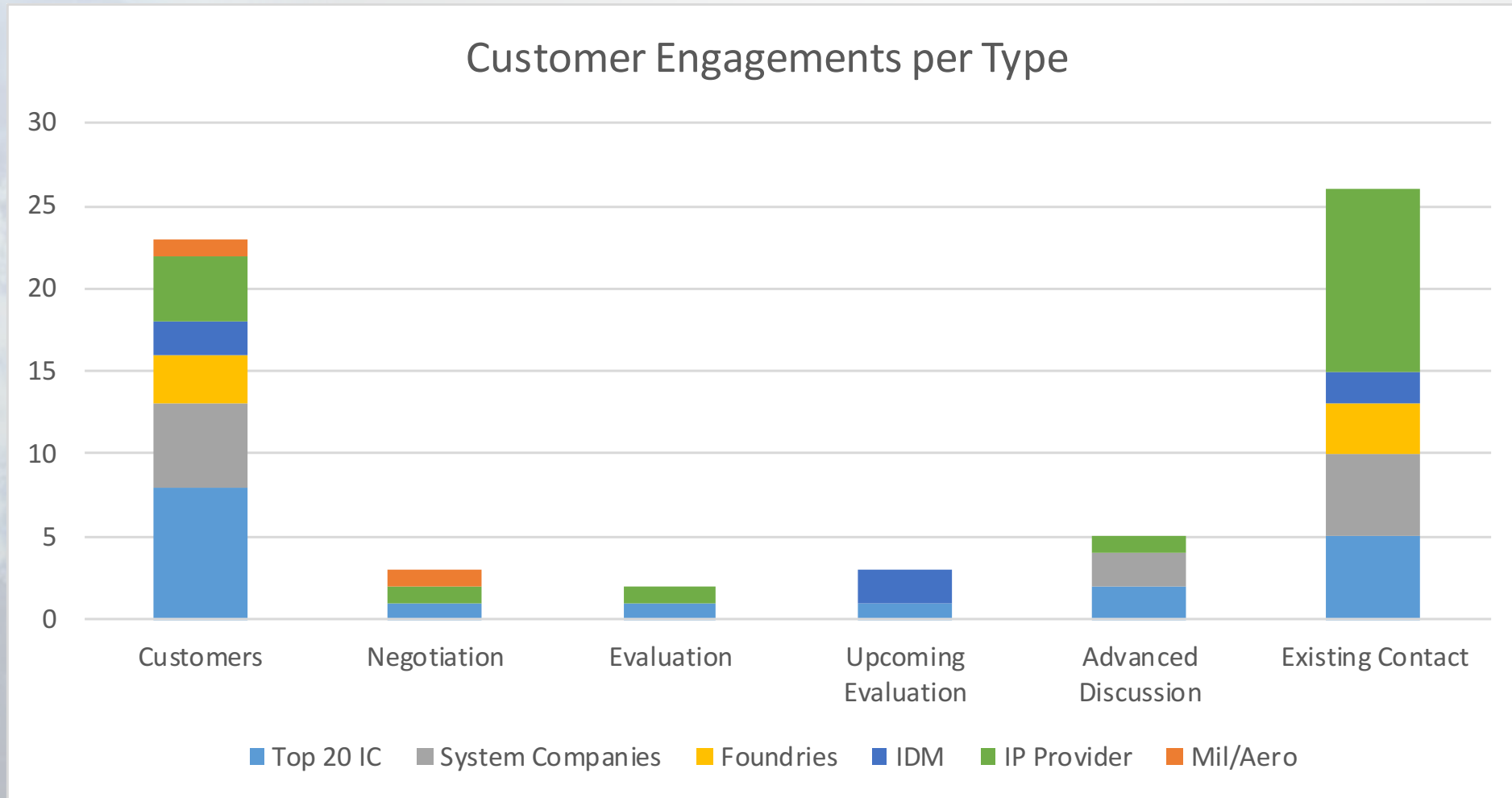
Customer Engagements

Geographical Breakdown



Customer Engagements

Customer Types Breakdown



IP quality not your problem?

This and other common myths debunked



Trust your IP supplier to check it all?

- Your design, your problem. Who's paying for the re-spin?
- Only Crossfire incoming inspection ensures your IP supplier, foundry or in-house colleagues have done their job

IP issues are trivial anyway?

- Some are – but they still cause delays
- Can you find them in 4TB of IP data?
- Many are far from trivial – how do you trace back the right related power pin in a large SPICE netlist?



IP is clean if the tools read it?

- EDA tools could not care less about missing arcs, wrong conditions or related power pins
- Only IP that passes your golden Transport quality deck can be called clean

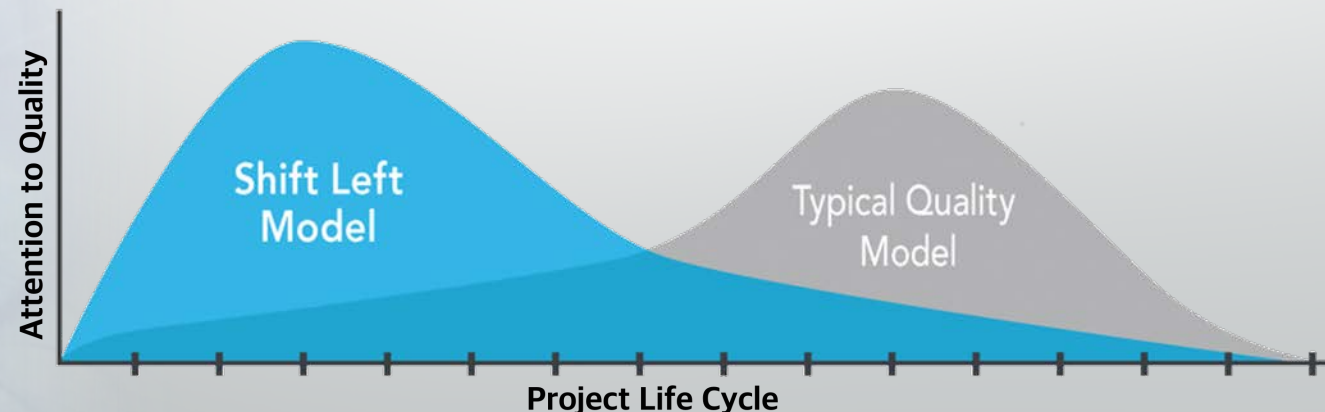
Final verification will catch it all?

- LVS is the worst part in the design cycle to reveal IP issues – high complexity, no time left for fixing
- Timing and power verification check vs the Liberty model, not vs SPICE

Crossfire usage model



- **Shift-left:** as part of regression testing, quality throughout project life cycle
- **Sign-off:** at the end of the project life cycle
- **Internal/External IP:** Ensure collateral consistency for IPs developed internally or provided by 3rd-party vendors





FRACTAL
Technologies

Thank you!



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Quality in Design Formats

www.fract-tech.com